



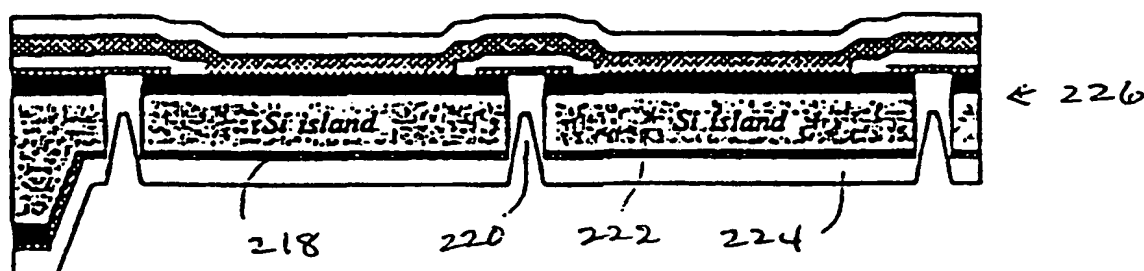
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(54) Title: FLEXIBLE SKIN INCORPORATING MEMS TECHNOLOGY

RIE etches backside using Al mask. Polyimide processing on backside.



Simplified process flow of the new flexible skin technology

(57) Abstract

A flexible skin formed of silicon islands (222) encapsulated in a polyimide film (214, 224). The silicon islands (222) preferably include a MEMS device and are connected together by a polyimide film (214, 224) (preferably about 1–100 μm thick). To create the silicon islands (222), silicon wafers (202) are etched to a desirable thickness (preferably about 10–500 μm) by Si wet etching and then patterned from the back side (200) by reactive ion etching (RIE).

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FLEXIBLE SKIN INCORPORATING MEMS TECHNOLOGY**Field of the Invention**

The invention relates to flexible materials, and more particularly to flexible skins which include microelectromechanical systems (MEMS) devices.

Background of the Invention

Microelectromechanical systems (MEMS) devices include semiconductor chips which include microfabricated mechanical systems on the chip, such as sensors.

For some applications, it is desirable to obtain real-time 2-D profiling of certain physical parameters such as temperature, force, pressure or shear stress on a 3-D object. If the surface of the object is flat, this profiling can be achieved by using a monolithic MEMS device with a large amount of sensors. However, such MEMS devices are typically rigid and flat. Profiling becomes much more difficult if the surface is not flat.

For example, in aerodynamics study, research objects such as an airfoil have non-planar and high-curvature surfaces. Previous attempts to achieve real-time distribution measurement embedded the discrete sensors on a surface. However, large sensor size and difficulty in packaging, i.e., plumbing and wiring, have long been limiting factors to realizing good measurements.

Barth et al. in 1985 reported a one-dimensional flexible Si-diode temperature sensor array in which a polyimide strip was used as a flexible material connecting Si islands formed by isotropic hydrofluoric, nitric, and acetic acid ("HNA") etching. Here, polyimide

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refers to a polymer of imide compounds, those that contain the =NH group. However, this technology needed some major improvements before it could be applied to more complicated sensor systems.

5 In 1994, Beebe and Denton presented their effort on improving the robustness and reliability of flexible polyimide skins which did not include any devices. The authors identified a major cause of failure as breaks in thin silicon on the island periphery. The methods used
10 to enhance the robustness, including the application of tape and coating of epoxy on both front and back sides of the skins, were all performed manually as post-processing steps. These methods are not an ideal solution for a reliable as well as mass-producible smart skin
15 technology.

Bang and Pan have an on-going project to develop a flexible heat-flux sensor array which is made by direct deposition of thin-film metals on commercial Kapton™ substrates. A large array of metal temperature sensors
20 can be made in this way, but neither ICs nor silicon MEMS are easily integrated with this approach. Hence, only limited types of sensors are available using this approach and a hybrid assembly of electronic circuits is not readily avoidable.

25 Summary of the Invention

The present disclosure provides a flexible MEMS technology to produce "smart skins" with integrated MEMS devices that can be easily affixed to non-planar surfaces.

30 The invention provides the integration of MEMS devices on a flexible skin through a new microfabrication technology. Many individual Si islands are used for

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silicon MEMS/electronics devices. These Si islands are connected together by a polyimide film. To create the Si islands, Si wafers are etched and then patterned from the back side.

5 The present invention includes a new flexible skin technology that is compatible with both IC and MEMS fabrications. Skin reliability is greatly improved by a strong periphery of the silicon islands formed by vertical reactive ion etching ("RIE"). Moreover, the
10 inventors have realized one embodiment as a 2-D flexible skin integrated with shear stress sensors. These flexible skins can be about 3 cm long and about 1 cm wide, and include about 100 sensors. The skin polyimide is about 17 μm thick and the silicon islands are about 75
15 μm thick. These skins have been successfully taped on a semi-cylindrical (about 1.3 cm diameter) delta wing leading edge to perform real-time 2-D shear stress profiling. This has allowed experimental real time detection of the air flow boundary layer over the leading
20 edge of a delta wing.

Brief Description of the Drawings

FIGS. 1A, 1B, and 1C illustrate a prior art method of forming Si islands on a polyimide film.

FIGS. 2A, 2B, and 2C illustrate the preferred
25 technique of forming Si islands.

FIGS. 3A to 3H illustrate a fabrication process for a flexible shear stress sensor skin according to the present invention.

FIG. 4 shows calibration results of a sensor of a
30 flexible skin on a flat surface.

FIG. 5 is a schematic of a delta wing.

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FIG. 6 shows a cross section of a flexible skin on a delta wing edge.

FIG. 7 shows averaged output from one row of sensors on a flexible skin according to the present invention.

FIG. 8 shows RMS fluctuation used to identify a flow separation point.

FIG. 9 shows a comparison of separation lines measured by a single sensor and a flexible shear stress sensor array according to the present invention.

Detailed Description

The inventors believe that many of the failures of flexible skins in prior art systems were caused by thin peripheries on Si islands. These thin peripheries break during squeezing and folding tests. As shown in FIG. 1A, forming Si islands 100 by isotropic HNA etching resulted in thin and weak Si island peripheries 102.

In comparison, as shown in FIGS. 1B and 1C demonstrate the structural difference that is obtained when etching with caustic anisotropic etchants such as tetramethylammonium hydroxide ("TMAH") or potassium hydroxide ("KOH"). This etching forms Si islands 104 which are much more robust when subjected to squeezing and folding. The Si islands 104 formed are in a trapezoidal shape. The resulting Si islands 104 have island peripheries 106 that are thicker and stronger than the corresponding island peripheries 102 shown in FIG. 1A. As shown in FIG. 1C, the combination of anisotropic etching and reactive ion etching ("RIE") results in Si islands 108 which have island peripheries 110 which may be even thicker and stronger. The RIE etching removes

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the edge portions of the Si islands 108 leaving island peripheries 110 which are substantially vertical.

Unfortunately, these caustic etchants attack all types of polyimides, which would damage exposed layers of polyimide, such as polyimide layers 112 in FIG. 1B and polyimide layers 114 in FIG. 1C. Hence part of the technique includes extra selectivity in etching. One way is by using coated polyimide layers after the islands are formed. Alternately, other protection from the etchants during the etching can be used. Gold or other expensive metals can be used as the protection materials. A one-sided etching device can be used. However, this alternative is not only costly, but also incompatible with IC processing. A one-sided etching apparatus is not easily usable since the pressure difference between the two sides can cause problems, including the rupture of the polyimide near the end of the etching. Moreover, any small leak on the wafer near the end of the etching might also allow attack of the polyimide on the front side.

FIGS. 2A to 2C show the intermediate steps of the preferred mode of fabrication for forming flexible skins.

The process starts with FIG. 2A which uses selective TMAH or KOH etching on the back side 200 of a Si wafer 202. Silicon nitride 204 is used as a mask. This etching brings the Si wafer 202 to a desired thickness, preferably about 10-500 μm .

As shown in FIG. 2B, a first aluminum layer 206 is then evaporated on silicon nitride 205 on the front side 208 of the Si wafer 202. The first aluminum layer 206 is then patterned. A first polyimide layer 210 is spun-on (preferably about 1-100 μm thick), cured and patterned to cover the patterned first aluminum layer 206 completely. Conventional aluminum metallization then follows to form

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a second aluminum layer 212 which is used for electrical metal leads. A second polyimide layer 214 is spun-on and patterned to form bonding pads 216.

As shown in FIG. 2C, SF₆-based vertical RIE
5 etching is performed on the back side 200 of the Si wafer 202 using aluminum 218 as masking material. The top layer of aluminum 206 serves as an etch stop in the RIE etching. This RIE etching removes a vertical block of Si material, hence forming "streets" 220 defining Si islands
10 222.

A third polyimide layer 224 is spun-on and cured on the aluminum mask 218, at least partially filling the streets 220, to cover both sides of the Si islands 222. Since the islands are formed by RIE, they may have the
15 shape shown in FIG. 1B or FIG. 1C. This process leaves finished flexible skins 226 in a Si wafer frame. The flexible skins 226 are then preferably cut off from the remaining Si wafer frame by a razor blade.

Because the above description was to explain the
20 concepts involved, the flexible skin described did not contain sensors or other IC devices. However, the above process is compatible with IC processing because the above process involves only aluminum and polyimide which are commonly used in IC fabrication. Using a wafer with
25 fabricated IC and/or MEMS devices (without metallization), the above process needs only minimal adjustment to produce flexible MEMS skins. An example of such flexible MEMS skins are discussed below.

In the above process of FIGS 2A to 2C, the Si
30 islands 222 are formed by vertical RIE etching performed on the Si wafer 202. Because the Si wafer 202 thickness is less than about 100 μm after the initial etching (see FIG. 2A) and the RIE etching is substantially vertical,

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Si islands 222 can be well-defined with dimensions as small as about 100 μm and spaced less than about 50 μm apart. Such sizes make it possible for the flexible skin to be applied on a very high-curvature surface with good conformal coverage. Good coverage of very high-curvature surfaces may be more difficult if the island shaping techniques shown in FIG. 1A are used because, e.g., HNA etching is isotropic. TMAH or KOH etching as shown in FIG. 1B can be used, however it may require large corner compensation structures to avoid fast undercut on corners.

The preferred embodiment uses polyimide as flexible skin material. This material has good mechanical strength and flexibility. Preferably, DuPont's™ PI-2808™ polyimide is used which has a tensile strength of about 210 MPa. This value is one of the highest among the spin-coated polyimides presently available. This is almost as high as the tensile strength of Kapton™ which is also a product made by DuPont™. Kapton™'s tensile strength is about 231 MPa and commonly used as a flexible runner for providing reliable connections to moving print heads.

In the above process shown in FIGS. 2A to 2C, the thickness of each of the polyimide layers 210, 214 on the front side of the Si wafer 202 is about 3-4 μm after curing at about 350° C. Because the polyimide layers 210, 214 have not been exposed to strong acids or bases during the above process, their mechanical properties should not be degraded. Accordingly, a total of about 7 μm of polyimide on the front side of the Si wafer 202 provides the ability to withstand a tensile force of about 1.47 kg/mm, which is fairly strong.

However, the peel-off forces of polyimide from Si

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substrates given by the manufacturer is only about 0.23 g/mm. Therefore, even a small shear force exerted on a Si island might peel the Si island away from the polyimide.

5 A preferred solution to this problem is to spin a thick polyimide layer 224, e.g., of about 10 μm , on the back side of the Si wafer 202 to fully encapsulate the Si islands 222. This raises the maximum tensile force that the flexible skin 226 can stand.

10 As shown in FIGS. 2A to 2C, the second aluminum layer 212 used to form metal leads is completely embedded in two polyimide layers 210, 214 on the front side of the flexible skin 226. Also, the Si islands 222 on which the aluminum layers 206, 212 are resting do not have weak
15 edges. Therefore, the Si islands 222 should be able to stand repetitive squeezing and bending without breaking. By way of example, in initial testing of flexible skins constructed according to the technique described above, no metal lead failure was observed after more than 100
20 times of 90°-180° bending.

 If for some reason the first polyimide layer 206 can not be used, the first layer can be other IC-compatible low temperature dielectric materials such as low temperature oxide ("LTO"). Moreover, if some more
25 advanced Si dry etching technologies, such as deep RIE, are available, the etching of the back side 200 of the Si wafer 202 by KOH or TMAH can be replaced by such advanced dry etching techniques. In this case, the etching can also be delayed until before the third aluminum layer 218
30 is deposited.

 In a preferred embodiment of the present invention, a flexible shear stress sensor array is constructed according to the method described below.

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This embodiment shows a useful MEMS device integrated on a flexible substrate using the flexible skin technology described herein.

A shear stress sensor is a vacuum-insulated diaphragm-type thermal sensor capable of measuring wall shear stress exerted by viscous flow. Such sensors are useful in turbulent flow study. For example, arrays of shear stress sensors on a rigid substrate have been used in the past to detect real-time shear stress distribution on a flat (i.e., 2-D) surface for flow-induced drag reduction study. However, the application of shear stress sensor arrays have been limited to 2-D flow because of limitations in flexible packaging. A flexible shear stress sensor array will allow extending that application to 3-D flow. In addition, the fabrication of shear stress sensors is a surface micromachining process with reasonably high complexity. The preferred embodiment is a good demonstration of the compatibility between the flexible skin of the present invention and MEMS technologies.

FIGS. 3A to 3H show steps of the fabrication process flow of a preferred embodiment of a flexible shear stress sensor array (348 in FIG. 3H). This fabrication process combines elements of the fabrication processes of the shear stress sensor and the skin.

First, as shown in FIG. 3A, a front side 300 of a silicon wafer 302 is etched through local oxidation of silicon (LOCOS). This oxidation leaves thermal oxide 304 in recesses 306 on the front side 300 of the Si wafer 302. A layer of silicon nitride 308 is applied to cover the front side 300 of the Si wafer 302 including the thermal oxide 304.

In FIG. 3B, phosphosilicate glass ("PSG") 310 is deposited over the silicon nitride layer 308. The PSG 310 is patterned to cover the underlying thermal oxide 304 deposits to form PSG sacrificial layers 310.

5 In FIG. 3C, a nitride diaphragm 312 is deposited on top of the PSG sacrificial layers 310. Etch holes 314 are opened in the nitride diaphragm 312 exposing an end of each of the PSG sacrificial layers 310. In FIG. 3E, the PSG sacrificial layers 310 and underlying thermal
10 oxide 304 are selectively etched to create cavities 316. Using a low pressure chemical vapor deposition (LPCVD) low temperature oxide (LTO)/nitride oven, LTO cavity seals 318 are placed in each of the etch holes 314 creating vacuum cavities 320 in the cavities 316 which
15 held the PSG 310 and thermal oxide 304. In FIG. 3E, a polysilicon layer 322 is then deposited on top of the nitride diaphragm 312. The polysilicon layer 322 is doped and patterned to remain only above the vacuum cavities 320. A thin nitride layer 324 is deposited for
20 passivation.

Similar to the process described above with reference to FIGS. 2A to 2C, in FIG. 3F, TMAH or KOH is used to etch the back side 326 of the Si wafer 302 to a desired thickness, preferably about 10-500 μm . The
25 layers on the front side of the Si wafer 302 are protected by the LTO 318 and nitride 324.

In FIG. 3G, a first aluminum layer 328 is deposited and patterned. A first polyimide layer 330 is deposited over the patterned first aluminum layer 328.
30 Contacts 332 are opened after the first aluminum/polyimide processing, so these contacts 332 are fresh and clean for the following metallization. A second aluminum layer 334 is deposited, leaving the

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contacts 332 open. Then a second polyimide layer 336 is deposited and patterned to cover areas above the first aluminum layer 328, forming bonding pads 338. In FIG. 3H, RIE etching is performed on the back side of the Si wafer 302 using a third aluminum layer 340 as masking material. The patterned first aluminum layer 328 act as etch stops. The RIE etching removes Si to form streets 342 between Si islands 344. Finally, a polyimide layer 346 is deposited on the back side of the Si wafer 302 , covering the third aluminum layer 340 and partially filling the streets 342 between Si islands 344. Preferably the total thickness of the polyimide layers 330, 336, 346 is about 1-100 μm .

One embodiment of the present invention has been constructed according to the technique described above referring to FIGS 3A to 3H as a flexible skin integrating shear stress sensors. The flexible skin is about 1 cm wide, about 3 cm long, and includes two 32-sensor rows with a horizontal pitch of about 635 μm and many other test devices (a total of more than 100 sensors). The two sensor rows are about 5 mm apart and located between the pairs of white square boxes in the picture. Their bonding pads are extended to left and right edges of the flexible skin. The layout is designed in such a way that each sensor row spans a semi-cylindrical surface (about 1.3 cm in diameter) of a delta wing leading edge under study with an angular resolution of about 5.6° . Each sensor occupies about $250 \times 250 \mu\text{m}^2$ and the dimensions of each Si island are about $450 \mu\text{m} \times 550 \mu\text{m}$ (about 75 μm thick) to fully accommodate one sensor and to achieve excellent surface smoothness and conformability.

A flexible MEMS skin technology has been developed and which is compatible with IC processing. A major

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failure of conventional technologies has been eliminated through the proper shaping of Si islands according to the present invention. Moreover, using the process of the present invention, Si islands as small as 100 μm can be
5 defined with good accuracy, which allows the skins to be applied on small surfaces with large curvatures. A first application of this technology has produced a flexible shear stress sensor array that was used in aerodynamics for the real-time measurement of shear stress
10 distribution on 3-D surfaces.

Test Results

As an example of the application of the technique described above, flexible shear-stress sensor skins for aerodynamics study have been fabricated according to the
15 preferred method of the present invention. Accordingly, the embodiment described below and the corresponding test results are demonstrative of one embodiment and are not restrictive limitations.

The finished skin is about 3 cm long and about 1
20 cm wide, including about 100 sensors. The skin polyimide is about 17 μm thick and the silicon islands are about 75 μm thick. These skins have been successfully taped on a semi-cylindrical (about 1.3 cm diameter) delta wing leading edge to perform real-time 2-D shear stress
25 profiling.

To test the shear stress sensor skin, the skin is first flush-mounted on a wind-tunnel where controlled shear flow is available for sensor calibration. The sensors on the flexible skin have been found to behave
30 the same as those on rigid substrates. FIG. 4 shows calibration results where the square of the output voltage is proportional to the one-third power of shear

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stress τ and the sensitivity is about 100 mV/Pa under constant temperature bias. Next, the skins are mounted on one of the two 3-D leading edges of a delta wing. The leading edge is divided into many blocks of removable
5 semi-cylinders 2 cm long and 1.3 cm in diameter. The sensor block is recessed by about 100 μm to compensate for the skin thickness.

At this point, only one row of sensors is used to measure the instantaneous distribution of shear stress on
10 that location. By moving the skins along the whole leading edge block by block, we are able to map out the steady-state shear stress distribution.

FIG. 6 shows a schematic of the cross section of a packaged sensor block for delta wing test. First, two
15 skins are glued to the semi-cylinder with the sensors on the curved surface and the bonding leads/pads extended to the flat surface. Then a circuit board with pre-soldered wires is attached to the flat surface next to the bonding pads of the skins. Ultrasonic wire bonding is performed
20 to connect the leads to the circuit board and then the bonding wires are fixed by epoxy. Here, the circuit board is a piece of Si with gold bonding and soldering pads specially designed and fabricated for this purpose.

Measurements have been done under different flow
25 velocities (U), skin locations (L) and angles of attack (AOA, defined as the angle between the air flow and the delta wing plane). For example, FIG. 7 shows the averaged output voltages (after gain of 10) of the sensors for $U = 30$ m/s, $L = 29$ cm and $\text{AOA} = 30^\circ$. Sensor
30 locations are indicated by θ , which is 0° at the bottom surface and 180° at the top surface. The averaged shear stress has a minima at about 110° , which is a result of flow over the cylinder.

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The flow separation point is where the flow boundary layer starts to separate from the leading edge surface. Determining the flow separation point is non-trivial. Typically, the surface shear stress fluctuation stays low before separation, and rises sharply after separation. Therefore, a flow separation point can be identified through measurement of the root-mean-square (RMS) value of shear stress fluctuation. FIG. 8 shows the RMS results of one measurement, from which the separation point is found to be at $\theta \approx 80^\circ$. These results reflect the experimental determination of flow separation points in real time. The separation line along the leading edge is consistent with the data measured from a single shear stress sensor that was placed around the leading edge point by point in steady state flow (FIG. 9). Based on this data, for real time flow control, a single sensor is no longer adequate. Flexible shear stress arrays are necessary because the flow separation point along the leading edge is a function of changing U, L and AOA in real flow field.

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Claims

What is claimed is:

1. A flexible microelectronic device,
comprising:
 - 5 a plurality of silicon islands;
a lower polyimide layer which covers a lower
surface of each of said silicon islands and at least
partially fills gaps between silicon islands;
an upper polyimide layer which covers an upper
10 surface of each silicon island.
2. The device of claim 1 where the silicon
islands are formed by vertical reactive ion etching
applied to a silicon wafer.
3. The device of claim 1 which further comprises
15 aluminum patches which extend from the upper surface of
each silicon island to the upper surface of that silicon
island's neighboring silicon island and which are
positioned between the upper surface of the silicon
islands and the upper polyimide layer.
- 20 4. The device of claim 1 which further comprises
aluminum patches extending across each gap and which are
positioned between the upper surface of the silicon
islands and the upper polyimide layer.
5. The device of claim 1 which further comprises
25 at least one MEMS device.
6. A method of manufacturing a flexible

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microelectronic device comprising:

- first etching a lower side of a wafer;
- depositing a first layer of aluminum on an upper side of the wafer;
- 5 patterning the first layer of aluminum;
- depositing a first layer of polyimide on the upper side of the wafer, covering the first layer of aluminum;
- depositing a second layer of aluminum on the upper side of the wafer, covering the first layer of polyimide;
- 10 depositing a second layer of polyimide on the upper side of the wafer, covering the second layer of aluminum;
- depositing a third layer of aluminum on the lower side of the wafer;
- 15 patterning the third layer of aluminum;
- second etching the lower side of the wafer using the third layer of aluminum as a mask and the first layer of aluminum as an etch stop, such that the wafer is divided into islands with gaps surrounding each island;
- 20 and
- depositing a third layer of polyimide on the lower side of the wafer, such that the gaps are at least partially filled.

7. The method of claim 6 where the first etching
25 is TMAH etching.

8. The method of claim 6 where the first etching
 is KOH etching.

9. The method of claim 6 where the second
 etching is vertical reactive ion etching.

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10. The method of claim 6 which further comprises forming at least one MEMS device on the upper side of at least one of the islands.

11. The method of claim 10 where at least one of
5 the MEMS devices is a shear stress sensor.

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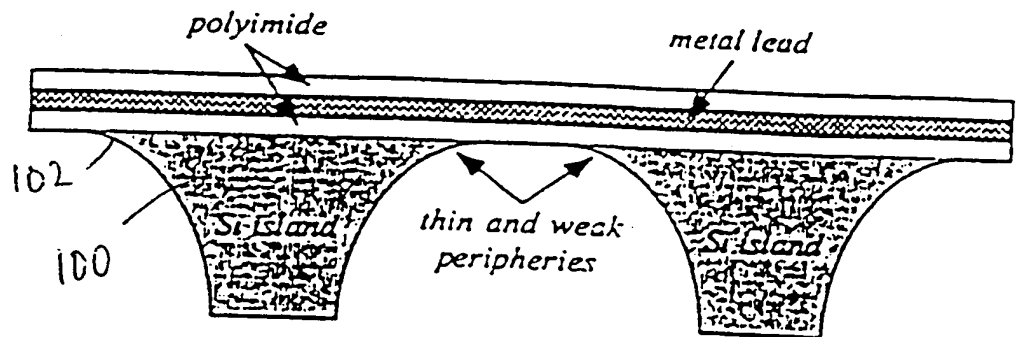


Fig. 1A

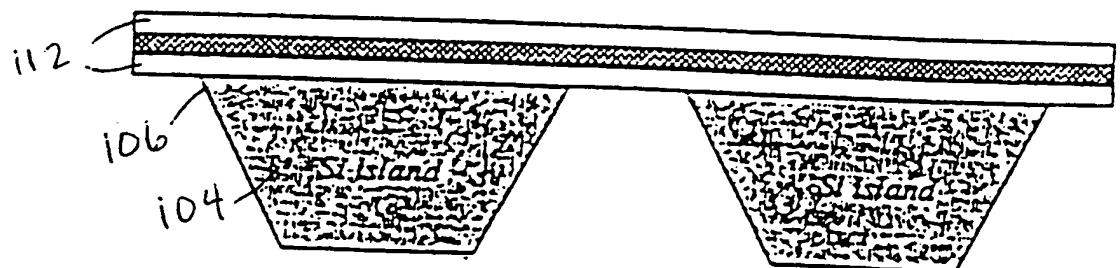
~~Fig. 1A~~ by HNA isotropic etching.

Fig. 1B

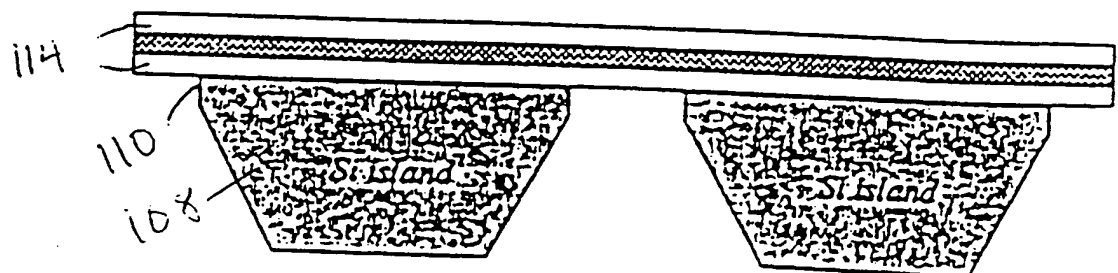
~~Fig. 1B~~ by anisotropic etching (TMAH or KOH).

Fig. 1C

~~Fig. 1C~~ by a combination of anisotropic etching and RIE.~~Fig. 1~~ Si island shapes formed by different ways.

(PRIOR ART)

Fig. 2A X TMAH or KOH selectively etches backside.

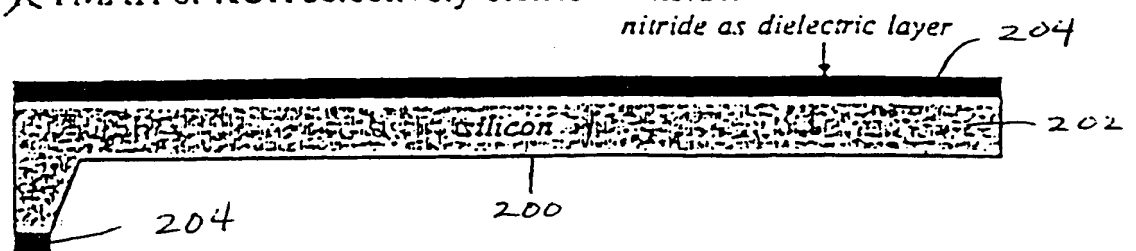


Fig. 2B X Aluminum/polyimide/aluminum/polyimide processing on frontside.

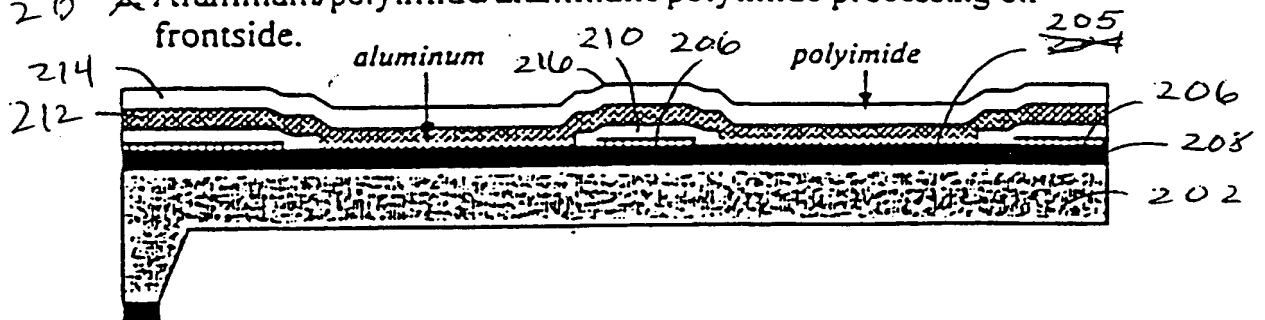


Fig. 2C X RIE etches backside using Al mask. Polyimide processing on backside.

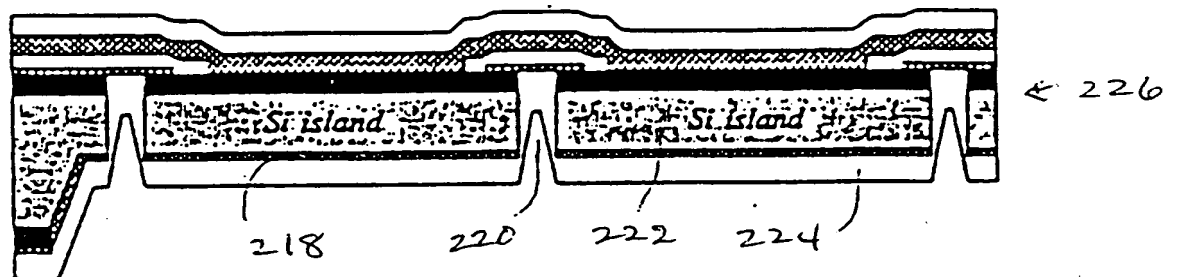


Fig. 2 Simplified process flow of the new flexible skin technology

Fig. 3A X Local oxidation (LOCOS). thermal oxide silicon nitride
 300 308 304 306
 302 silicon

Fig. 3B X Deposit and pattern PSG sacrificial layer.
 310 PSG

Fig. 3C X Deposit nitride diaphragm and open etch holes.
 314 312

Fig. 3D X Sacrificial layer etch and cavity seal in LPCVD LTO/nitride furnaces.
 LTO 316 320 vacuum cavity

Fig. 3E X Deposit, dope and pattern polysilicon. Deposit thin nitride for passivation.
 324 322 polysilicon

Fig. 3F X TMAH or KOH etches backside (front side protected with LTO/nitride).
 302 326

Fig. 3G X Aluminum/polyimide processing + contact opening + aluminum/polyimide processing on frontside.
 335 330 328 336 334 332 polyimide
 aluminum

Fig. 3H X RIE etches backside using Al mask. Polyimide processing on backside.
 340 346 348 344 342

~~Fig. 3~~ ~~Fig. 3~~ Fabrication process flow of the flexible shear stress sensor skin.

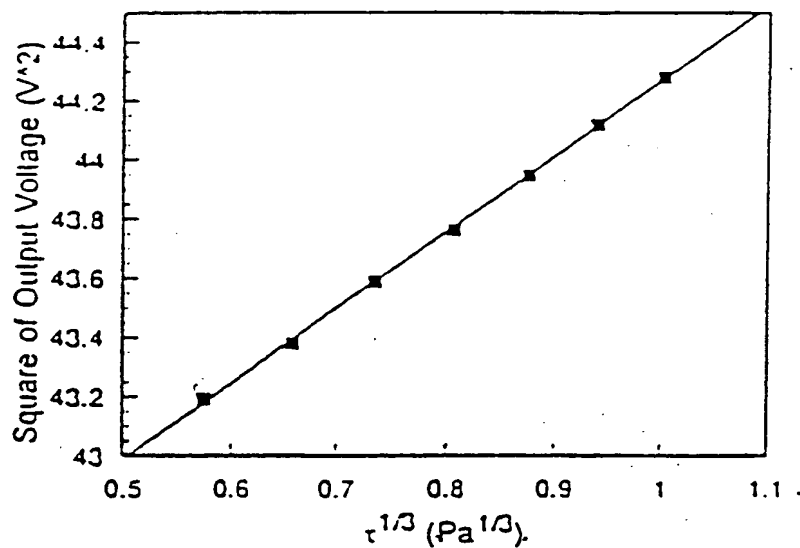


Fig. 4 ~~Fig. 3~~ Calibration curve of a sensor on a flexible skin mounted on a flat surface. The sensor is biased in constant temperature mode.

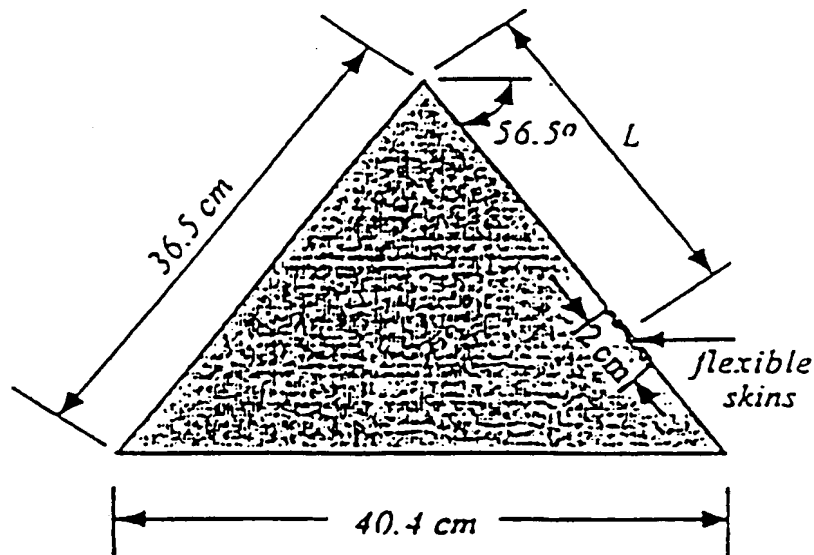


Fig. 5

~~Fig. 2~~ Schematic of the delta wing.

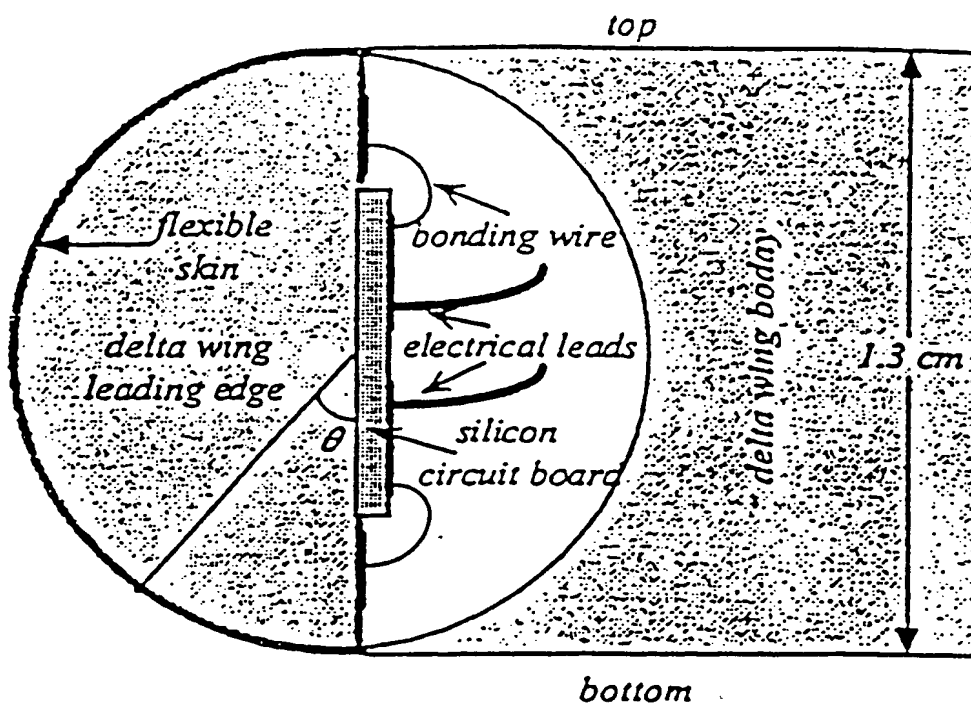


Fig. 10 Packaging scheme for the flexible shear stress sensor skin on delta wing edge.

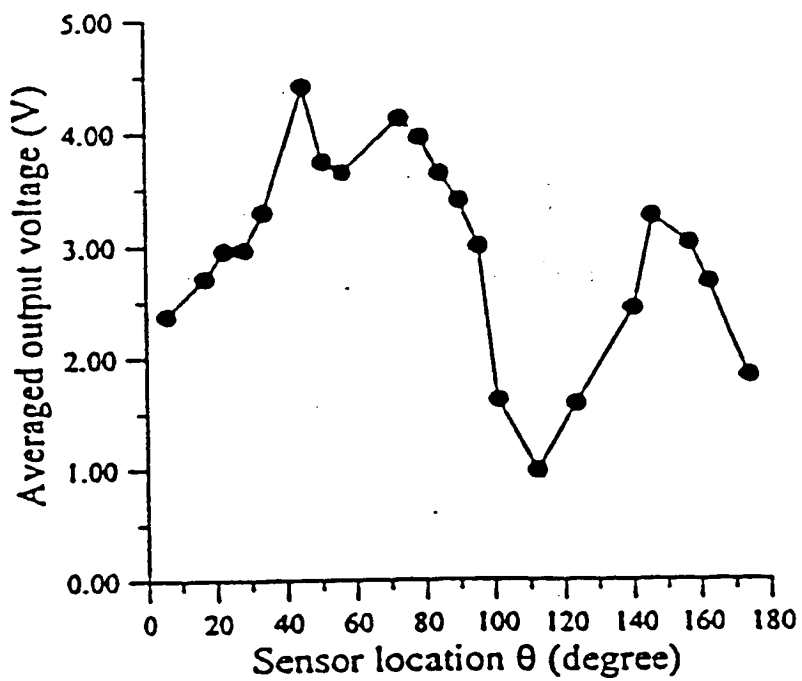


Fig. 14

Fig. 14 Averaged output from one row of sensors on a skin.

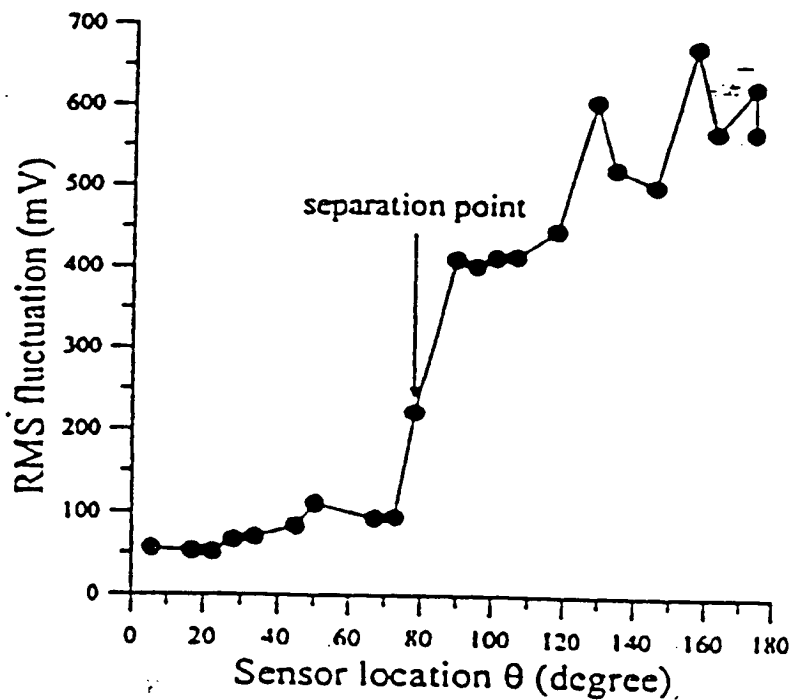


Fig. 8

~~Fig. 15~~ RMS fluctuation used to identify separation point.

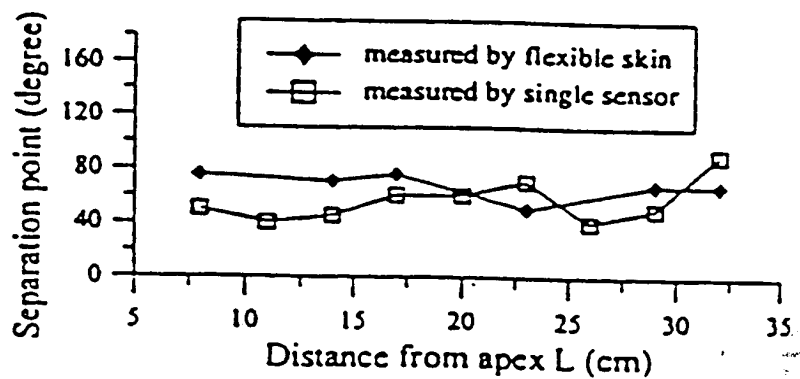


Fig. 9

~~Fig. 16~~ Comparison of separation lines measured by a single sensor and a flexible shear stress sensor array.

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US98/01510

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) :B32B 3/00
US CL :428/195

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 428/195, 201, 209, 213, 446, 689, 699

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A, E	US 5,736,750 A (YAMAZAKI et al.) 07 April 1998, see abstract.	1-11
A, P	US 5,648,277 A (ZHANG et al.) 15 July 1997, see abstract.	1-11

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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Date of the actual completion of the international search

27 MAY 1998

Date of mailing of the international search report

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